

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1012	@ad<"20001205" and (synchronization adj3 master)	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:28
L2	10	1 and (tdm adj3 bus) and clock and counter and frame and period	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:28
L3	0	2 and dummy adj period	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:28
L4	0	2 and dummy	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:28
L5	12	@ad<"20001205" and dummy adj period	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:58
L6	23	dummy adj period	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:35
L7	2	("370"/\$.ccls. "375"/\$.ccls.) and dummy adj period	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:49
L8	27	("370"/\$.ccls. "375"/\$.ccls.) and dummy adj3 period	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:42
L9	6	8 and master	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:42
L10	1	"5544324".pn.	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:49
L11	182	@ad<"20001205" and (tdm adj3 bus) and master and clock	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:58
L12	153	@ad<"20001205" and ("370"/\$.ccls. "375"/\$.ccls.) and (tdm adj3 bus) and clock and counter and frame	US-PGPUB; USPAT	OR	OFF	2005/08/01 16:59
L13	10	@ad<"20001205" and ("370"/\$.ccls. "375"/\$.ccls.) and (tdm adj3 bus) and clock and counter and frame and (synchronization adj3 master) and (identify period)	US-PGPUB; USPAT	OR	OFF	2005/08/01 17:12
L14	0	13 and (carry adj3 bit)	US-PGPUB; USPAT	OR	OFF	2005/08/01 17:12

Day : Monday  
Date: 8/1/2005  
Time: 15:45:29

## PALM INTRANET

### Inventor Name Search Result

Your Search was:

Last Name = WEGO

First Name = ARILD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10538722</u>	Not Issued	019	01/01/0001	METHOD FOR REDUCING THE BUS LOAD IN A SYNCHRONOUS DATA BUS SYSTEM	WEGO, ARILD
<u>10526526</u>	Not Issued	019	01/01/0001	SWITCHING ARRANGEMENT INCLUDING TIME-SLOT BUSES AND SERVERAL BUFFERS	WEGO, ARILD
<u>10521388</u>	Not Issued	020	01/14/2005	METHOD AND ARRANGEMENT FOR REDUCING PHASE JUMPS WHEN SWITCHING BETWEEN SYNCHRONISATION SOURCES	WEGO, ARILD
<u>10500048</u>	Not Issued	030	06/25/2004	METHOD FOR REDUCING THE AMOUNT OF NEEDED MEMORY IN A TDM SWITCH SYSTEM	WEGO, ARILD
<u>10497858</u>	Not Issued	030	06/07/2004	RATE ADAPTION WITHIN A TDM SWITCH USING BIT STUFFING	WEGO, ARILD
<u>10000038</u>	Not Issued	030	12/04/2001	ARRANGEMENT AND METHOD FOR TRANSMITTING DATA OVER A TDM BUS	WEGO, ARILD

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="Wego"/>	<input type="text" value="Arild"/>	<input type="button" value="Search"/>

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